



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Application of: )

Applicants: Paul Vogt, *et al.* )

Serial No.: 09/825,818 )

Filed: 04/04/2001 )

Title: Circuit And Method For Hiding )  
Peer Devices In A Computer Bus )

Examiner: Thomas J. Cleary

Group Art Unit: 2181

Confirmation No.: 2921

2181  
11/11/03  
4-13-04  
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APR 08 2004

Technology Center 2100

Commissioner for Patents  
MAIL STOP FEE AMENDMENT  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT

In response to the Office Action dated as mailed October 31, 2003 and having a response due date of March 31, 2004, with a two-month extension of time, please amend the application as follows: